

Expedition PCB Functionality Matrix

	Pinnacle	Ascent LX	Ascent
Design Capture / DesignView	Sold Separately	Sold Separately	Sold Separately
Automatic Routing			
Layer Limits	No Limit	No Limit	
Memory / Fanout / Route	X	X	N/A
AutoActive Routing			
Fanout / Plow / Route selected netlines	X	X	X
Multiplow	X	HS option	N/A
Test Point Assignment			
Interactive Test Point Placement	X	X	X
DFM Option	X	Option	N/A
Auto Assign Test Points	X	option	N/A
Via Minimization	X	option	N/A
Trace Spread	X	option	N/A
Trace Expand	X	option	N/A
Advanced Interconnect Design			
Build-up / Microvia Rule Definition	X	X	X
Build-up / Microvia Interactive Routing	X	X	X
Advanced Interconnect Option	Option	Option	N/A
Build-up / Microvia Autorouting	option	option	N/A
High Speed Design			
High Speed Rules Definition	X	X	X
Signal Analyzer/Vision Interface	X	X	X
High Speed Option	X	Option	N/A
Automatic Tuning	X	option	N/A
Automatic Differential Pair Routing	X	option	N/A
AutoActive Tuning	X	option	N/A
AutoActive Route/Plow Differential Pairs	X	option	N/A
Hazard Review of HS Violations	X	option	N/A
Layout Options			
Design Reuse	Option	Option	N/A
TeamPCB	Option	Option	N/A
Variant Manager	Option	Option	Option
Bi-Directional Interfaces			
DXF	X	X	X
VB ASCII	X	X	X
IDF 2.0/3.0	X	Option	Option
IFF/Agilent ADS Interface	Option	Option	N/A
Import Interfaces			
PADS ASCII Reader	X	X	X

EDIF Netlist	X	X	X
Export Interfaces			
VB Report Writer	X	X	X
BETAsoft / HyperLynx / XTK / Incases	X	X	X
Manufacturing Outputs			
Automatic Test Equipment (ATE)	X	X	X
Automatic Insert (AIS)	X	X	X
Gerber Out	X	X	X
NC Drill	X	X	X
GerbTool, Designer, Read/Edit Gerber	X	Option	Option
Manufacturing Outputs ToolBox	X	Option	Option
C-LINK, CAE Technologies	X	option	option
FabMaster	X	option	option
GenCAD, Mitron	X	option	option
CAM Output Manager Administrator	Option	Option	Option
CAM Output Manager Client	Option	Option	Option
Scepter Panelization	Option	Option	Option